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| #5 | (substrate<sentence>coupling and substrate<sentence>noise and substrate<sentence>model* and matrix<IN>metadata) |
| #6 | (substrate<sentence>coupling<sentence>noise)<paragraph>model* and (scalable<sentence>parameter?) |
| #7 | (substrate<sentence>coupling<sentence>noise)<paragraph>model* and (scalable<sentence>parameter?) |
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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. An efficient modeling approach for substrate noise coupling analysis
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[Circuits and Systems, 2002. ISCAS 2002. IEEE International Symposium on](#)
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Digital Object Identifier 10.1109/ISCAS.2002.1010684
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- ☐ 2. A comprehensive geometry-dependent macromodel for substrate noise coupling in heavily
processes
Ozis, D.; Fiez, T.; Mayaram, K.;
[Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE 2002](#)
12-15 May 2002 Page(s):497 - 500
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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. Wavelets modelling of inductance of interconnect in digital circuits of mixed-signal ICs
Wang Jun;
[Microwave Conference Proceedings, 2005. APMC 2005. Asia-Pacific Conference Proceedings](#)
Volume 2, 4-7 Dec. 2005 Page(s):5 pp.
Digital Object Identifier 10.1109/APMC.2005.1606515
[AbstractPlus](#) | Full Text: [PDF](#)(1680 KB) IEEE CNF
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Chou, M.; White, J.;
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- ☐ 5. A simple transmission line de-embedding method for accurate RF CMOS noise modeling
Guo, J.C.; Huang, C.H.; Lien, W.Y.; Wu, C.M.;
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Digital Object Identifier 10.1109/RFIC.2004.1320694
[AbstractPlus](#) | Full Text: [PDF](#)(381 KB) IEEE CNF

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- ☐ 6. **An automated and efficient substrate noise analysis tool**
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[Rights and Permissions](#)
- ☐ 7. **Stable and efficient reduction of large, multiport RC networks by pole analysis via congruence**
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Volume 16, Issue 7, July 1997 Page(s):734 - 744
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[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(372 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ 8. **Numerically stable Green function for modeling and analysis of substrate coupling in integrated circuits**
Niknejad, A.M.; Gharpurey, R.; Meyer, R.G.;
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[Rights and Permissions](#)
- ☐ 9. **Efficient techniques for accurate modeling and simulation of substrate coupling in mixed-signal ICs**
Costa, J.P.; Mike Chou; Silveira, L.M.;
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Volume 18, Issue 5, May 1999 Page(s):597 - 607
Digital Object Identifier 10.1109/43.759076
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(520 KB) IEEE JNL
[Rights and Permissions](#)
- ☐ 10. **An efficient formulation for substrate parasitic extraction accounting for nonuniform current distribution**
Cheng-gang Xu; Fiez, T.; Mayaram, K.;
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[Rights and Permissions](#)
- ☐ 11. **Modeling techniques and verification methodologies for substrate coupling effects in mixed-signal chip designs**
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- ☐ 12. **Block structure preserving model order reduction**
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[Rights and Permissions](#)
- ☐ 14. Isolation strategy against substrate coupling in CMOS mixed-signal/RF circuits
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- ☐ 16. Substrate noise analysis with compact digital noise injection and substrate models
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- ☐ 17. Characterization of SSN Coupling to Signal Via in Multi-layer PCBs and Packages
Jongbae Park; Hyungsoo Kim; Jun So Pak; Jounggho Kim;
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- ☐ 19. SuPREME: substrate and power-delivery reluctance-enhanced macromodel evaluation
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 23-26 Feb. 1998 Page(s):892 - 898
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 Ching-Han Tsai; Sung-Mo Kang;
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 Volume 19, Issue 2, Feb. 2000 Page(s):253 - 266
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 Digital Object Identifier 10.1109/SOC.2003.1241521
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 IEEE CNF IEEE Conference Proceeding
 IEE CNF IEE Conference Proceeding
 IEEE STD IEEE Standard

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- ☐ 1. MOS transistor modelling for RF IC design
 Enz, C.; Cheng, Y.;
[Solid-State Circuits, IEEE Journal of](#)
 Volume 35, Issue 2, Feb. 2000 Page(s):186 - 201
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- ☐ 5. Parasitic modeling and noise mitigation in advanced RF/mixed-signal silicon germanium pnc
 Singh, R.; Tretiakov, Y.V.; Johnson, J.B.; Sweeney, S.L.; Barry, R.L.; Kumar, M.; Erturk, M.; Katzer
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- ☐ 6. **Accurate modeling and parameter extraction for meander-line N-well resistors**
Murji, R.; Deen, M.J.;
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Digital Object Identifier 10.1109/TED.2005.850637
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- ☐ 7. **Simulation and measurement of supply and substrate noise in mixed-signal ICs**
Owens, B.E.; Adluri, S.; Birrer, P.; Shreeve, R.; Arunachalam, S.K.; Mayaram, K.; Fiez, T.S.;
[Solid-State Circuits, IEEE Journal of](#)
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- ☐ 8. **CAD tools for embedded analogue circuits in mixed-signal integrated systems on chip**
Gielen, G.G.E.;
[Computers and Digital Techniques, IEE Proceedings-](#)
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- ☐ 9. **An efficient modeling approach for substrate noise coupling analysis**
Ozis, D.; Mayaram, K.; Fiez, T.;
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Volume 5, 26-29 May 2002 Page(s):V-237 - V-240 vol.5
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[Custom Integrated Circuits Conference, 2002. Proceedings of the IEEE, 2002](#)
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- ☐ 11. **Design procedure for fully integrated 900 MHz medium power amplifiers in 0.6 μ m CMOS technology on epi-substrate**
Baureis, P.; Peter, M.; Hein, H.; Oehler, F.;
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File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
<u>Ozis</u> , Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

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Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. Ds
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L6: Entry 1 of 4

File: PGPB

Nov 17, 2005

PGPUB-DOCUMENT-NUMBER: 20050254106

PGPUB-FILING-TYPE: corrected

DOCUMENT-IDENTIFIER: US 20050254106 A9

TITLE: Scanning device for coded data

PUBLICATION-DATE: November 17, 2005

PRIOR-PUBLICATION:

DOC-ID

DATE

US 0190092 A1

September 30, 2004

INVENTOR-INFORMATION:

NAME

CITY

STATE

COUNTRY

Silverbrook, Kia

Balmain

AU

Lapstun, Paul

Balmain

AU

Rusman, Jan

Balmain

AU

Henderson, Peter Charles Boyd

Balmain

AU

Moini, Alireza

Balmain

AU

Yourlo, Zhenya Alexander

Balmain

AU

Underwood, Matthew John

Balmain

AU

Ridley, Nicholas Damon

Balmain

AU

US-CL-CURRENT: [358/539](#); [358/474](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Drawings
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☐ 2. Document ID: US 20040190092 A1

L6: Entry 2 of 4

File: PGPB

Sep 30, 2004

PGPUB-DOCUMENT-NUMBER: 20040190092

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040190092 A1

TITLE: Scanning device for coded data

PUBLICATION-DATE: September 30, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Silverbrook, Kia	Balmain		AU
Lapstun, Paul	Balmain		AU
Rusman, Jan	Balmain		AU
Henderson, Peter Charles Boyd	Balmain		AU
Moini, Alireza	Balmain		AU
Yourlo, Zhenya Alexander	Balmain		AU
Underwood, Matthew John	Balmain		AU
Ridley, Nicholas Damon	Balmain		AU

US-CL-CURRENT: 358/539; 358/474

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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PGPUB-DOCUMENT-NUMBER: 20040190085

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040190085 A1

TITLE: Sensing device for coded data

PUBLICATION-DATE: September 30, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Silverbrook, Kia	Balmain		AU
Lapstun, Paul	Balmain		AU
Henderson, Peter Charles Boyd	Blamain		AU
Moini, Alireza	Balmain		AU
Yourlo, Zhenya Alexander	Balmain		AU
Underwood, Matthew John	Balmain		AU
Ridley, Nicholas Damon	Balmain		AU
Rusman, Jan	Balmain		AU

US-CL-CURRENT: 358/474; 358/539

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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☐ 4. Document ID: US 20040117162 A1

L6: Entry 4 of 4

File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new
DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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Clear	Generate Collection	Print	Fwd Refs	Bkwd Refs	Generate OACS
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Term	Documents
SCALABLE	29226
SCALABLES	0
PARAMETER?	0
PARAMETERA	6
PARAMETERB	5
PARAMETERC	1
PARAMETERD	4
PARAMETERE	7
PARAMETERF	1
PARAMETERH	2
PARAMETERJ	4
(L4 AND SCALABLE AND PARAMETER?).PGPB,USPT.	4

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☐ 1. Document ID: US 20040117162 A1

Using default format because multiple data bases are involved.

L7: Entry 1 of 1

File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMC	Draw. De
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Clear

Generate Collection

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Generate OACS

Term	Documents
Z	509103
ZES	53
PARAMETER?	0
PARAMETERA	6
PARAMETERB	5
PARAMETERC	1
PARAMETERD	4
PARAMETERE	7
PARAMETERF	1
PARAMETERH	2

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Search Results - Record(s) 1 through 12 of 12 returned.

☐ 1. Document ID: US 20040117162 A1

Using default format because multiple data bases are involved.

L8: Entry 1 of 12

File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: 703/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw. De
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☐ 2. Document ID: US 20020022951 A1

L8: Entry 2 of 12

File: PGPB

Feb 21, 2002

PGPUB-DOCUMENT-NUMBER: 20020022951

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020022951 A1

TITLE: Method, apparatus and computer program product for determination of noise in mixed signal systems

PUBLICATION-DATE: February 21, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Heijningen, Marc Van	Leuven		BE
Badaroglu, Mustafa	Leuven		BE

US-CL-CURRENT: 703/16

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Drawn De
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☐ 3. Document ID: US 20010029601 A1

L8: Entry 3 of 12

File: PGPB

Oct 11, 2001

PGPUB-DOCUMENT-NUMBER: 20010029601

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20010029601 A1

TITLE: Semiconductor device analyzer, method for analyzing/manufacturing semiconductor device, and storage medium storing program for analyzing semiconductor device

PUBLICATION-DATE: October 11, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Kimura, Tomohisa	Tokyo		JP
Okumura, Makiko	Kanagawa		JP

US-CL-CURRENT: 716/19; 716/4

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Drawn De
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☐ 4. Document ID: US 7016820 B2

L8: Entry 4 of 12

File: USPT

Mar 21, 2006

US-PAT-NO: 7016820

DOCUMENT-IDENTIFIER: US 7016820 B2

TITLE: Semiconductor device analyzer, method for analyzing/manufacturing semiconductor device, and storage medium storing program for analyzing semiconductor device

DATE-ISSUED: March 21, 2006

PRIOR-PUBLICATION:

DOC-ID	DATE
US 20010029601 A1	October 11, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kimura; Tomohisa	Tokyo			JP
Okumura; Makiko	Kanagawa			JP

US-CL-CURRENT: 703/2; 700/121, 703/14, 716/4

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 5. Document ID: US 6941258 B2

L8: Entry 5 of 12

File: USPT

Sep 6, 2005

US-PAT-NO: 6941258

DOCUMENT-IDENTIFIER: US 6941258 B2

TITLE: Method, apparatus and computer program product for determination of noise in mixed signal systems

DATE-ISSUED: September 6, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Van Heijningen; Marc	Leuven			BE
Badaroglu; Mustafa	Leuven			BE

US-CL-CURRENT: 703/16; 703/14, 703/15, 703/17, 703/2, 716/6

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 6. Document ID: US 6920417 B2

L8: Entry 6 of 12

File: USPT

Jul 19, 2005

US-PAT-NO: 6920417

DOCUMENT-IDENTIFIER: US 6920417 B2

TITLE: Apparatus for modeling IC substrate noise utilizing improved doping profile access key

DATE-ISSUED: July 19, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lescot; Jerome D.	Grenoble			FR
Marchand; Bertrand L.	Grenoble			FR

US-CL-CURRENT: 703/13; 438/510, 703/2

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw De
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☐ 7. Document ID: US 6895344 B2

L8: Entry 7 of 12

File: USPT

May 17, 2005

US-PAT-NO: 6895344

DOCUMENT-IDENTIFIER: US 6895344 B2

TITLE: Extracting resistances for a substrate network

DATE-ISSUED: May 17, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ramaswamy; Sridhar	Plano	TX		

US-CL-CURRENT: 702/57; 455/3.02, 716/19, 716/6

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 8. Document ID: US 6725185 B2

L8: Entry 8 of 12

File: USPT

Apr 20, 2004

US-PAT-NO: 6725185

DOCUMENT-IDENTIFIER: US 6725185 B2

**** See image for Certificate of Correction ****

TITLE: Apparatus for modeling IC substrate noise

DATE-ISSUED: April 20, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Clement; Francois J. R.	Neuchatel			CH

US-CL-CURRENT: 703/14; 438/210, 438/510, 703/2, 716/4

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw. De
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☐ 9. Document ID: US 6480986 B1

L8: Entry 9 of 12

File: USPT

Nov 12, 2002

US-PAT-NO: 6480986

DOCUMENT-IDENTIFIER: US 6480986 B1

TITLE: IC substrate noise modeling including extracted capacitance for improved accuracy

DATE-ISSUED: November 12, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Richer; Jean-Michel	Voiron			FR

US-CL-CURRENT: 716/4; 438/510, 716/1

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw De
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☐ 10. Document ID: US 6438733 B1

L8: Entry 10 of 12

File: USPT

Aug 20, 2002

US-PAT-NO: 6438733

DOCUMENT-IDENTIFIER: US 6438733 B1

TITLE: IC substrate noise modeling with improved surface gridding technique

DATE-ISSUED: August 20, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Clement; Fran.cedilla.ois J. R.	Neuchatel			CH

US-CL-CURRENT: 716/4

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw De
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☐ 11. Document ID: US 6291324 B1

L8: Entry 11 of 12

File: USPT

Sep 18, 2001

US-PAT-NO: 6291324

DOCUMENT-IDENTIFIER: US 6291324 B1

TITLE: Method of modeling IC substrate noises utilizing improved doping profile access

DATE-ISSUED: September 18, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Lescot; Jerome D.	Grenoble			FR
Marchand; Bertrand L.	Grenoble			FR

US-CL-CURRENT: 438/510; 438/424

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWC	Draw De
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☐ 12. Document ID: US 6291322 B1

L8: Entry 12 of 12

File: USPT

Sep 18, 2001

US-PAT-NO: 6291322

DOCUMENT-IDENTIFIER: US 6291322 B1

** See image for Certificate of Correction **

TITLE: Method for modeling noises in an integrated circuit

DATE-ISSUED: September 18, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Clement; Fran.cedilla.ois J. R.	Neuchatel			CH

US-CL-CURRENT: 438/510; 438/210

Full	Title	Citation	Front	Review	Classification	Date	Reference	Substantive	Administrative	Claims	KWC	Draw. De
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Term	Documents
SUBSTRATE	815034
SUBSTRATES	335473
MODEL\$	0
MODEL	582924
MODELA	20
MODELAA	3
MODELABBRV	1
MODELABILITIES	1
MODELABILITY	21
MODELABLE	65
MODELABSTRACTION	1
(L4 AND (MODEL\$ WITH SUBSTRATE)).PGPB,USPT.	12

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☐ 1. Document ID: US 20050023656 A1

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L11: Entry 1 of 2

File: PGPB

Feb 3, 2005

PGPUB-DOCUMENT-NUMBER: 20050023656

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20050023656 A1

TITLE: Vertical system integration

PUBLICATION-DATE: February 3, 2005

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Leedy, Glenn J.	Saline	MI	US

US-CL-CURRENT: [257/678](#); [257/686](#), [438/106](#), [438/109](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw-D
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☐ 2. Document ID: US 20040117162 A1

L11: Entry 2 of 2

File: PGPB

Jun 17, 2004

PGPUB-DOCUMENT-NUMBER: 20040117162

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20040117162 A1

TITLE: Modeling substrate noise coupling using scalable parameters

PUBLICATION-DATE: June 17, 2004

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY
Ozis, Dicle	Seattle	WA	US
Mayaram, Kartikeya	Corvallis	OR	US
Fiez, Terri	Corvallis	OR	US

US-CL-CURRENT: [703/2](#)

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC	Draw-D
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